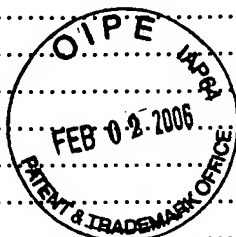


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/050,347
Filing Date January 15, 2002
Inventor Gurtej S. Sandhu et al.
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner Laura M. Schillinger
Attorney's Docket No. MI22-1897
Title: Methods of Forming Transistors



SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The attached Form PTO-1449 is submitted in compliance with 37 CFR §1.56. Pursuant to FEDERAL REGISTER, Vol. 69, No. 182, pg. 56542 (September 21, 2004), no copies of any cited U.S. patents or U.S. published applications are included herewith. Copies of all other cited art are attached. No admission is made regarding whether the listed references are prior art.

Respectfully submitted,

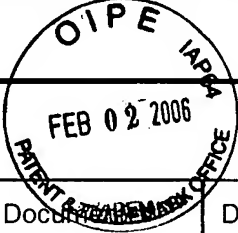
Dated:

February 2, 2006

By:

Jennifer J. Taylor
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Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-1897		SERIAL NO. 10/050,347	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Gurtej S. Sandhu et al.			
				FILING DATE January 15, 2002		GROUP 2813	



U.S. PATENT DOCUMENTS							
*Examiner Initial	Document Number	Date	Name	Class	Sub-class	Filing Date If Appropriate	
	AA	5,969,397	10-1999	Grider, III et al.			
	AB	6,040,249	03-2000	Holloway			
	AC	6,087,236	07-2000	Chau et al.			
	AD	6,245,616 B1	06-2001	Buchanan et al.			
	AE	6,150,226	11-2000	Reinberg			
	AF						

FOREIGN PATENT DOCUMENTS								
	Document Number	Date	Country	Class	Sub-class	Translation		
						Yes	No	
	AN							
	AO							
	AP							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)			
AS			
AT			

EXAMINER	DATE CONSIDERED
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.